

your BEST camera module partner

KLT-D3MA-IMX476 V2.0

20MP Sony IMX476 MIPI Interface Auto Focus Camera Module



Front View

Back View

Specifications

| Camera Module No. | KLT-D3MA-IMX476 V2.0 |
|--------------------------|--------------------------------------|
| Resolution | 20 MP |
| Image Sensor | IMX476 |
| Sensor Type | 1/2.78" |
| Pixel Size | 1.00 um x 1.00 um |
| EFL | 2.35 mm |
| F.NO | 2.2 |
| Pixel | 5216 x 4032 |
| View Angle | 117.3°(DFOV) 97.0°(HFOV) 78.7°(VFOV) |
| Lens Dimensions | 8.80 x 8.80 x 7.20 mm |
| Module Size | 21.00 x 8.80 mm |
| Module Type | Auto Focus |
| Interface | MIPI |
| Auto Focus VCM Driver IC | DW9763 |
| Lens Type | 650nm IR Cut |
| Operating Temperature | -20°C to +70°C |
| Mating Connector | BBR43-30KB533 |

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Top View



Side View





Bottom View

Mating Connector

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| AB | С | D | | E | |
|--|--------------------------------|--------------------------|-----------------|--|---|
| RoHS | | | Version | Information | |
| 0 SIGNAL | | | V 1.0 | First Version | |
| 1 GND | 4032 | | V2.0 | Change lens and VCM | |
| 2 GND | | | | | |
| 3 GND 4 GND | 5216 | | | | |
| 1 5 AFVDD 2.8V | §°° ⊂ O | 7.20±0.20(Micro Height) | | | 1 |
| 6 AFEN 7 SDA | | 7.00±0.20(Normal Height) | | | 1 |
| 8 DOVDD1.8V | <u>8.80±0.15</u> 2.95 | 4.05±0.20(VCM Height) | | | |
| 9 SCL | | | | | |
| 10 DVDD 1.05V 11 GND | 5.4 117.3° | | | | |
| 12 XSHUTDOWN | 5,4 117,3° | | | | |
| | | | | | |
| 14 NC O | | 0.20±0.05 | | | |
| 13 MCN 07.000 14 NC 1000 <td>6.00±0.10</td> <td>Steel Grounding</td> <td></td> <td></td> <td></td> | 6.00±0.10 | Steel Grounding | | | |
| | | | | | |
| 18 MCLK 19 MD0P | | FCCL, RA | | <u> </u> | |
| 20 GND | | h | 30 | | |
| 19 MD0P 20 GND 21 MD1N 22 NC | | BBR13-30K6417 | 29 | | |
| 23 MD1P | 8.50±0.10 | -10.20 ± 0.05 | | | |
| 24 AVDD 2.8V | | Steel Grounding | | 4.25 – | |
| 25 VPP(NC) 26 AGND | TOP VIEW | SIDE VIEW | BOTTOM | VIEW | |
| 27 MD2N | | | | | |
| 28 MD3N 29 MD2P | | | NOTE: | | |
| 30 MD21 | | | 1.The devic | e slave address:0x34; | |
| | | | 2.Driver IC | and its I2C Address: | |
| | | | DW9763; | | |
| Parameters: | 2 Long specifications | | , | · · · | |
| | 2 Lens specification: | Kai Lap Tea | hnoloa | jies Group Ltd | |
| 3 Sensor specification: | FOV: 117.3°(D);97°(H);78.7°(V) | | | | |
| Image Sensor: IMX476 | F/NO.: 2.2 | Designed By | Model Name | ⊵ KLT-D3MA-IMX476 V2.(| |
| Pixel: 1.0um×1.0um | TV distortion: <9.6% | Designed By Kevin | | $= 1 \times 1 = 0 \times 1 \times$ | J |
| Lens Type: 1/2.78 | Focal length: 2.35mm | | Projection Type | | |
| Important Voltage Description: DVDD1.05V | | Checked By Aouly_Yo | ın 🕀 🧲 | mm 1/25/2024 Scale: Sheet: Version: | - |
| (external power supply); | IR Cut Coating: 650nm±10nm@50% | | Third Angle | 1:1 1 of 1 1/0 | |
| AB | C | D | | E | |

SONY

Diagonal 6.475 mm (Type 1/2.78) 20Mega-Pixel CMOS Image Sensor with Square Pixel for Color Cameras

IMX476-AAJH5-C

General description and application

IMX476-AAJH5-C is a diagonal 6.475 mm (Type 1/2.78) 20 Mega-pixel CMOS active pixel type stacked image sensor with a square pixel array. It adopts Sony's back-illuminated and stacked CMOS image sensor to achieve high speed image capturing by column parallel A/D converter circuits and high sensitivity and low noise image (comparing with conventional CMOS image sensor) through the backside illuminated imaging pixel structure. R, G, and B pigment primary color mosaic filter is employed. It operates with four power supply: analog 2.8 V, digital 1.05 V and 1.8 V for input/output interface and achieves low power consumption.

In addition, this product is designed for use in cellular phone and tablet pc. When using this for another application, Sony Semiconductor Solutions Corporation does not guarantee the quality and reliability of product. Therefore, don't use this for applications other than cellular phone and tablet pc. Consult your Sony Semiconductor Solutions Corporation sales representative if you have any questions.

Functions and Features

- Back-illuminated and stacked CMOS image sensor
- ◆ Quad Bayer Coding color filter arrangement
- High Frame Rate 30fps@Full resolution(Quad Bayer Coding Re-mosaic) / 30fps@Quad Bayer Coding HDR / 60fps@2x2 Adjacent Pixel Binning(4:3) / 120fps@2x2 Adjacent Pixel Binning (4:3)
- High signal to noise ratio(SNR)
- Dual sensor synchronization operation
- Built-in 2D Dynamic Defect Pixel Correction
- Lens Shading Correction (LSC) //
- Built-in temperature sensor
- ♦ Output video format of RAW10/8, COMP8
- Quad Bayer Coding Re-mosaic function
- Quad Bayer Coding HDR function
- 2x2 Adjacent Pixel Binning function
- Two PLLs for independent clock generation for pixel control and data output interface
- CSI-2 serial data output (MIPI 2lane/4lane, Max. 2.3Gbps/lane, D-PHY spec. ver. 1.2 compliant)
- 2-wire serial communication (Supports I²C "Fast mode" and "Fast-mode Plus")
- ♦ 9.0K bit of OTP ROM for users

Sony Semiconductor Solutions Corporation reserves the right to change products and specifications without prior notice. This information does not convey any license by any implication or otherwise under any patents or other right. Application circuits shown, if any, are typical examples illustrating the operation of the devices. Sony Semiconductor Solutions Corporation cannot assume responsibility for any problems arising out of the use of these circuits.

1. General Description

The DW9763 is a single 10-bit DAC with 100mA output current sinking capability and embedded 8KByte eFlash memory. Designed for linear control of voice coil motors, the DW9763 is capable of operating voltage up to 3.3V.

The SAC (Smart Actuator Control) mode is applied to minimize the mechanical vibration. The SAC mode highly improves the actuator's settling time and tolerance coverage compared with conventional LSC (Linear Slope Control) mode. The DAC and eFlash are controlled via an I2C compatible serial interface.

The DW9763 incorporates with a POR (Power On Reset) circuit, power down mode. POR circuit gets to operate when VDD (supply power) turns on. The output current keeps 0mA until valid register value takes place. During the power down mode, it consumes current max.1uA.

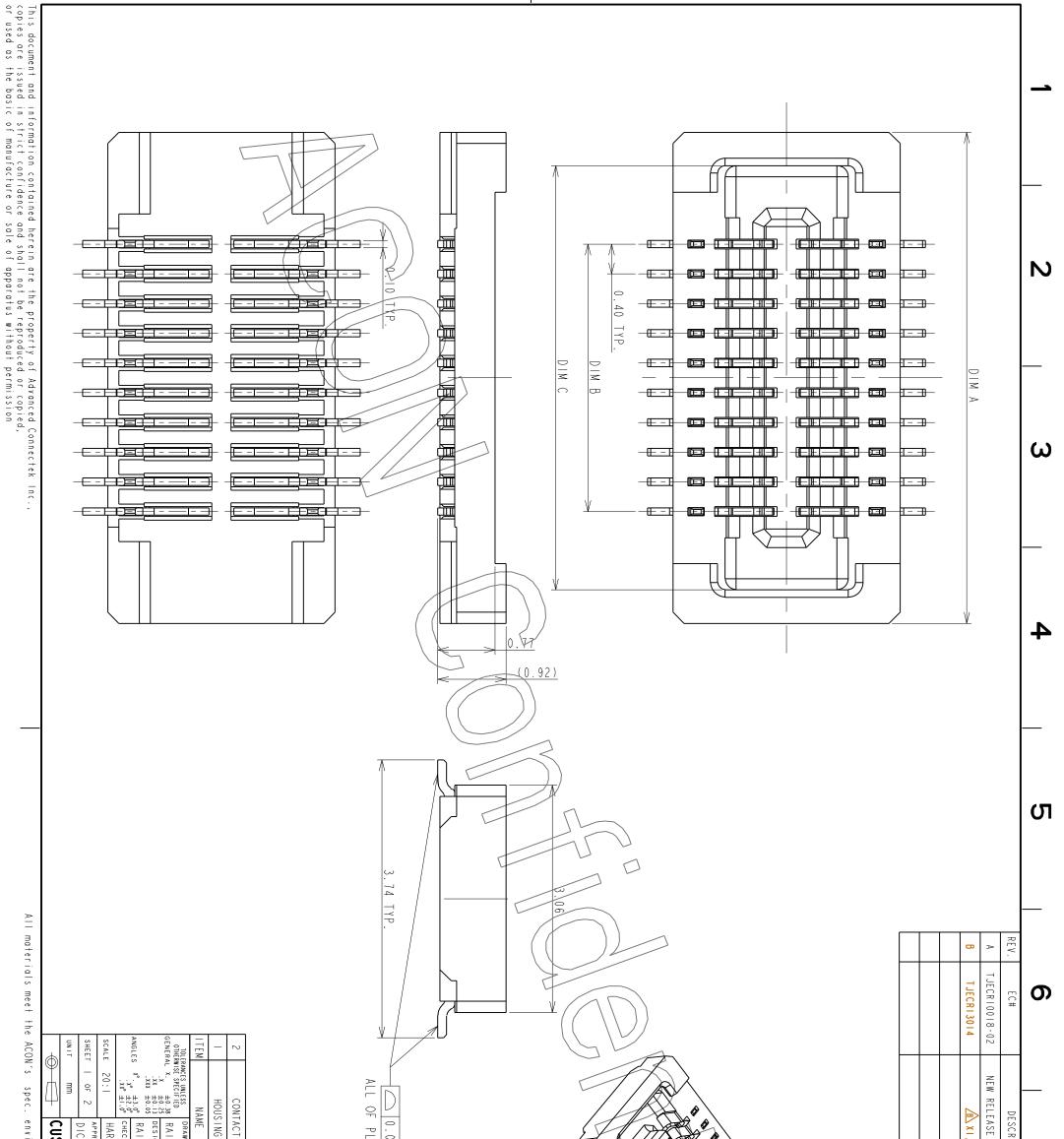
The DW9763 is designed for auto focus and optical zoom for mobile camera, digital still camera, camcorders and other nano actuator applications.

Features

10 bit resolution current sinking of 100mA for VCM SAC (Smart Actuator Control) mode Supply voltage range (VDD) : 2.3V to 3.3V Fast mode I2C interface compatible (1.8V interface available) Power down mode Power on reset (POR) Embedded 8KByte eFlash memory Package : 8 pin WLCSP Package Size : 0.77mm X 1.75mm X 0.3mm

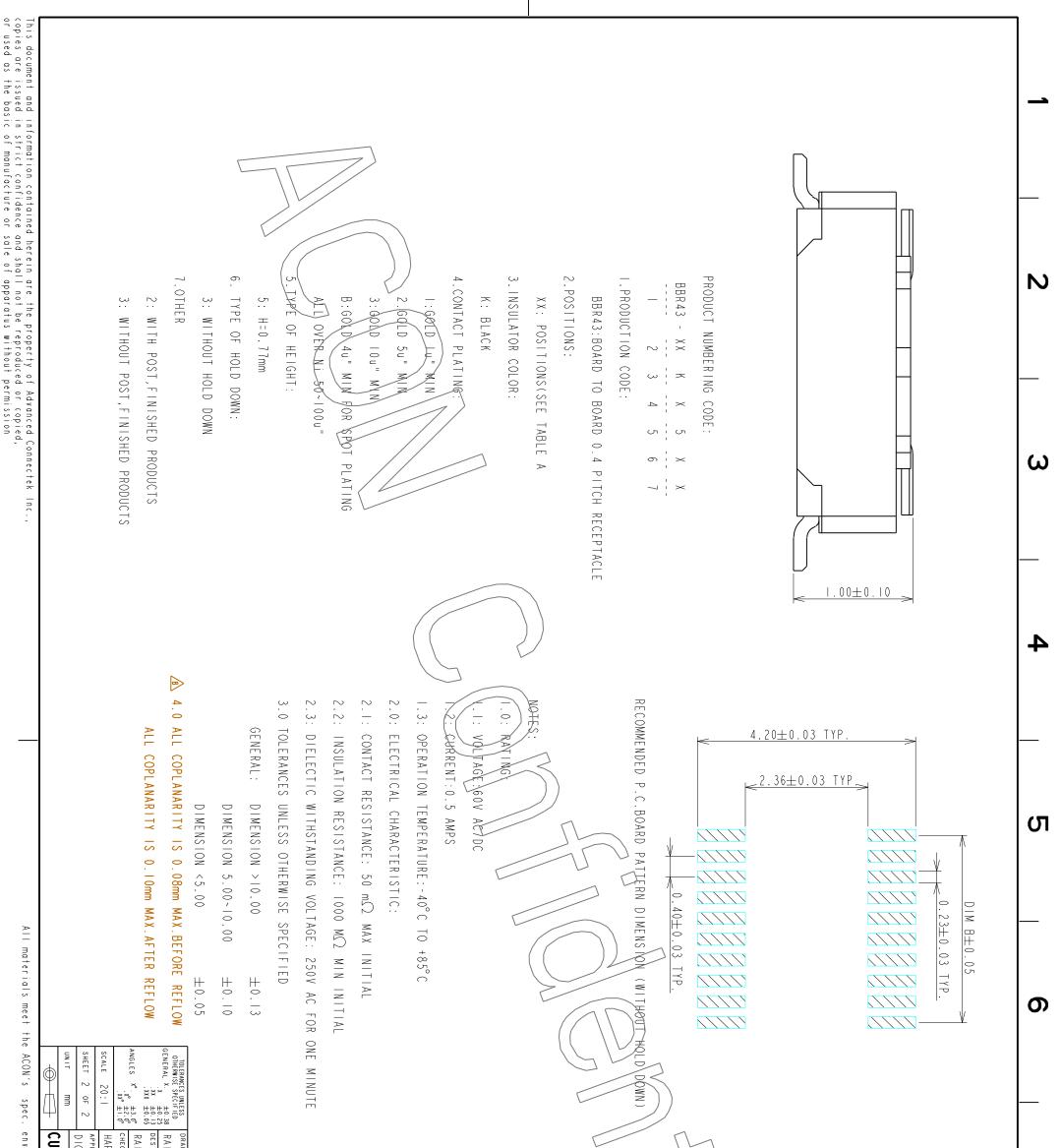
Applications

Mobile camera Digital still camera Camcorder Web camera Nano actuator



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| Π | | TITLE | 04/15/10 DATE | CHECKED |
| | | | | DESIGN |
| | | 10 | 04/15/10 | |
| | MATERIAL / FINISH | PART # | | 0 |
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| | COPPER ALLOY/PLATING GOLD | T - BBR 4 3 - 100 X 30 | ХХ Т-ВI | |
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CUSTOMER DRAWING DWG NO. DRAWN RAIN DESIGN RAIN HARDWARE HECKED wironment-related substances management technical standard ICK.LEE PROVED \Box DATE 04/24/10′ DATE 04/15/10′ DATE 04/15/10 ' 04/24/10' TABLE A: POSITIONS DATE 422 5 80 70 60 54 50 48 44 40 34 32 30 26 24 20 $\overline{0}$ ∞ SERIES . 4 10.61 $\overline{\infty}$ 12.21 8.61 A MIG 4 2 9.41 ف 7.41 7.01 6 -б. $\overline{\omega}$ 9 5 P0.4*H1.0mm BOARD TO BOARD CONN.RECEPTACLE WITHOUT HOLD DOWN . 0___ ~____ 2 6 . 6___ ∞ . ი . _____ . 6 | .4 C - BBR 4 3 - 0 4 - 0 I ADVANCED _ 7.60 6.40 6.00 DIM B 5 9 5.60 4 ς. \sim 13.60 0 ق 4 4 ω \sim BBR ∞ 60 20 40 . 00 60 . 80 80 20 40 60 . 4 . 60 60 .40 ||.7| |7.7| 15.71 12.51 |3.7| ||.3| 10.51 9.71 8.5I 8. . 7.71 6.91 6.5I 6. || 5.71 4.91 4.51 3.71 DIM C 5 . Ω I ZE A3 Ш D C \triangleright П

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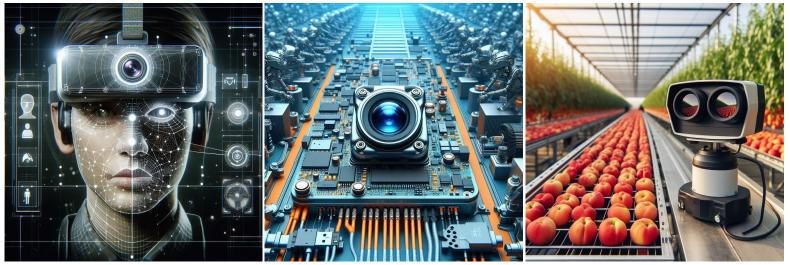
Cameras Applications







Video Conference



Live Streaming

Eye Tracker Biometric Detection

Machine Vision

Agricultural Monitor



Night Vision Security

Drone and Sports Eagle Eyes



Interactive Pet Camera

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Cameras Applications

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Camera Module Pinout Definition Reference Chart

| | ptina Himax GalaxyCore PixArt SmartSens Sensor |
|-----------------------------|---|
| Pin Signal | Description |
| DGND GND | ground for digital circuit |
| AGND | ground for analog circuit |
| PCLK DCK | DVP PCLK output |
| XCLR PWDN XSHUTDOWN STANDBY | power down active high with internal pull-down resistor |
| MCLK XVCLK XCLK INCK | system input clock |
| RESET RST | reset active low with internal pull-up resistor |
| NC NULL | no connect |
| SDA SIO_D SIOD | SCCB data |
| SCL SIO_C SIOC | SCCB input clock |
| VSYNC XVS FSYNC | DVP VSYNC output |
| HREF XHS | DVP HREF output |
| DOVDD | power for I/O circuit |
| AFVDD | power for VCM circuit |
| AVDD | power for analog circuit |
| DVDD | power for digital circuit |
| STROBE FSTROBE | strobe output |
| FSIN | synchronize the VSYNC signal from the other sensor |
| SID | SCCB last bit ID input |
| ILPWM | mechanical shutter output indicator |
| FREX | frame exposure / mechanical shutter |
| GPIO | general purpose inputs |
| SLASEL | I2C slave address select |
| AFEN | CEN chip enable active high on VCM driver IC |
| MIPI Interface | |
| MDN0 DN0 MD0N DATA N DMO1N | MIPI 1st data lane negative output |
| MDP0 DP0 MD0P DATA P DMO1P | MIPI 1st data lane positive output |
| MDN1 DN1 MD1N DATA2 N DMO2N | MIPI 2nd data lane negative output |
| MDP1 DP1 MD1P DATA2 P DMO2P | MIPI 2nd data lane positive output |
| MDN2 DN2 MD2N DATA3 N DMO3N | MIPI 3rd data lane negative output |
| MDP2 DP2 MD2P DATA3 P DMO3P | MIPI 3rd data lane positive output |
| MDN3 DN3 MD3N DATA4 N DMO4N | MIPI 4th data lane negative output |
| MDP3 DP3 MD3P DATA4 P DMO4P | MIPI 4th data lane positive output |
| MCN CLKN CLK_N DCKN | MIPI clock negative output |
| MCP CLKP MCP CLK P DCKN | MIPI clock positive output |
| DVP Parallel Interface | · ···· p · ···· p · ···· p |
| D0 D00 Y0 | DVP data output port 0 |
| D1 D01 Y1 | DVP data output port 1 |
| D2 D02 Y2 | DVP data output port 2 |
| D3 D03 Y3 | DVP data output port 3 |
| D4 D04 Y4 | DVP data output port 4 |
| D5 D05 Y5 | DVP data output port 5 |
| D6 D06 Y6 | DVP data output port 6 |
| D7 D07 Y7 | DVP data output port 7 |
| D8 D08 Y8 | DVP data output port 8 |
| D9 D09 Y9 | DVP data output port 8 DVP data output port 9 |
| D9 D09 19 D10 D010 Y10 | DVP data output port 9 DVP data output port 10 |
| D11 D011 Y11 | DVP data output port 10 |
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Camera Reliability Test

| Reliability Inspection Item | | | Testing Mathed | Acceptance Criteria | |
|-----------------------------|--|---|-------------------------|-------------------------|--|
| Cat | egory | Item | Testing Method | | |
| | Storage | High 60°C 96 Hours | Temperature Chamber | No Abnormal Situation | |
| | Temperature | Low -20°C 96 Hours | Temperature Chamber | No Abnormal Situation | |
| | Operation | High 60°C 24 Hours | Temperature Chamber | No Abnormal Situation | |
| Environmental | Temperature | Low -20°C 24 Hours | Temperature Chamber | No Abnormal Situation | |
| Environmentar | Humidity | 60°C 80% 24 Hours | Temperature Chamber | No Abnormal Situation | |
| | Thermal Shock | High 60°C 0.5 Hours Low -20°C 0.5 Hours Cycling in 24 Hours | Temperature Chamber | No Abnormal Situation | |
| | Drop Test | Without Package 60cm | 10 Times on Wood Floor | Electrically Functional | |
| | (Free Falling) | With Package 60cm 10 Times on Wood Floo | | Electrically Functional | |
| | Vibration Test | 50Hz X-Axis 2mm 30min | Vibration Table | Electrically Functional | |
| Physical | | 50Hz Y-Axis 2mm 30min | Vibration Table | Electrically Functional | |
| Filysical | | 50Hz Z-Axis 2mm 30min | Vibration Table | Electrically Functional | |
| | Cable Tensile Strength Test Cable Tensile Cycling in 24 Hours | | Tensile Testing Machine | Electrically Functional | |
| | ESD Test | Contact Discharge 2 KV | ESD Testing Machine | Electrically Functional | |
| Electrical | ESD Test | Air Discharge 4 KV | ESD Testing Machine | Electrically Functional | |
| | Aging Test On/Off 30 Seconds Cycling in 24 Hours | | Power Switch | Electrically Functional | |
| | USB Connector | On/Off 250 Times | Plug and Unplug | Electrically Functional | |



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Camera Inspection Standard

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| Inspection | | n Item | Inspection Method | Standard of Inspection | |
|------------|----------|------------------|-------------------|--|--|
| Category | | Item | порескоп месноа | | |
| | | Color | The Naked Eye | Major Difference is Not Allowed. | |
| | FPC/ PCB | Be Torn/Chopped | The Naked Eye | Copper Crack Exposure is Not Allowed. | |
| | | Marking | The Naked Eye | Clear, Recognizable (Within 30cm Distance) | |
| Appearance | | Scratches | The Naked Eye | The Inside Crack Exposure is Not Allowed | |
| | Holder | Gap | The Naked Eye | Meet the Height Standard | |
| | TIOIGEI | Screw | The Naked Eye | Make Sure Screws Are Presented (If Any) | |
| | | Damage | The Naked Eye | The Inside Crack Exposure is Not Allowed | |
| | | Scratch | The Naked Eye | No Effect On Resolution Standard | |
| | Lens | Contamination | The Naked Eye | No Effect On Resolution Standard | |
| | Lens | Oil Film | The Naked Eye | No Effect On Resolution Standard | |
| | | Cover Tape | The Naked Eye | No Issue On Appearance. | |
| | | No Communication | Test Board | Not Allowed | |
| | | Bright Pixel | Black Board | Not Allowed In the Image Center | |
| | | Dark Pixel | White board | Not Allowed In the Image Center | |
| | | Blurry | The Naked Eye | Not Allowed | |
| | | No Image | The Naked Eye | Not Allowed | |
| | | Vertical Line | The Naked Eye | Not Allowed | |
| | Image | Horizontal Line | The Naked Eye | Not Allowed | |
| Function | | Light Leakage | The Naked Eye | Not Allowed | |
| | | Blinking Image | The Naked Eye | Not Allowed | |
| | | Bruise | Inspection Jig | Not Allowed | |
| | | Resolution | Chart | Follows Outgoing Inspection Chart Standard | |
| | | Color | The Naked Eye | No Issue | |
| | | Noise | The Naked Eye | Not Allowed | |
| | | Corner Dark | The Naked Eye | Less Than 100px By 100px | |
| | | Color Resolution | The Naked Eye | No Issue | |
| | | Height | The Naked Eye | Follows Approval Data Sheet | |
| Dimer | ncion | Width | The Naked Eye | Follows Approval Data Sheet | |
| Dimer | 151011 | Length | The Naked Eye | Follows Approval Data Sheet | |
| | | Overall | The Naked Eye | Follows Approval Data Sheet | |

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KLT Package Solutions

KLT Camera Module

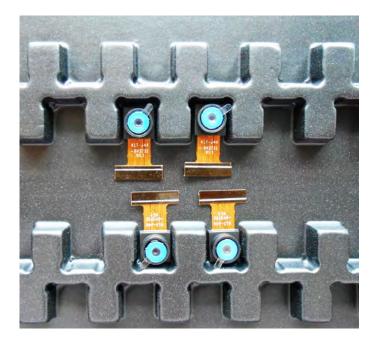


Tray with Grid and Space





Place Cameras on the Tray



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Camera Modules Package Solution

Full Tray of Cameras



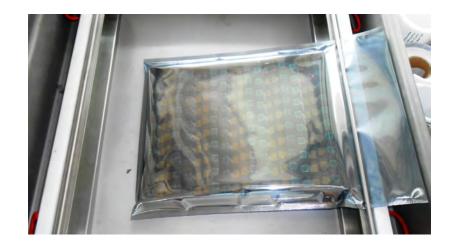
Put Tray into Anti-Static Bag

Cover Tray with Lid



Vacuum the Anti-Static Bag





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Camera Modules Package Solution

Sealed Vacuum Bag with Labels 1. Model and Description 2. Quantity 3. Shipping Date 4. Caution



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Large Order Package Solution

Place Foam Sheets Between Trays

Foam Sheets are Slightly Larger than Trays



Place Foam Sheets and Trays into Box

Foam Sheets are Tightly Fitting Box



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Small Order Package Solution

Place Foam Sheets and Trays into Small Box

Foam Sheets are Nicely Fitting the Small Box



Package in Small Box for Shipment

Place Small Boxes into Larger Box



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Carbon Box Package Solution

Seal the Carbon Box

Final Package Labelled Box



Carbon Box Ready for Shipment

1. Delivery Address and Phone No. 2. Box No. and Ship Date 3. Fragile Caution



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Sample Order Package Solution

Place Sample into Small Anti-Static Bag

Place Connectors into Small Ant-Static Bag





Sample Labels on the Small Bag 1. Camera Module or Connector Model 2. Shipping Date and Quantity 3. Caution



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Connectors Large Order Package Solution

Connectors in a Wheel



The Wheel is Perfectly Fitting the Box

Label Connectors in the Wheel



Connectors Box Ready for Shipment



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Company Kai Lap Technologies (KLT)

Kai Lap Technologies Group Limited. (KLT) was established in 2009, a next-generation technology driven manufacturer specialized in research, design, and produce of audio and video products. KLT is occupying 20,000 square feet automated plants with 100 employees of annual throughput 30,000,000 units cameras.

KLT provides OEM, ODM design, contract manufacturing, and builds the camera products. You may provide the requirements to us, even with a hand draft, our sales and engineering work together to meet your needs. We consider ourselves your last-term partner in developing practical and innovative solutions.

Our team covers everything from initial concept development to mass produced product. KLT specializes in customized camera design, raw material, electronic engineering, firmware/software development, product testing, and packing design. Our experienced strategic supply systems offer a robust and dependable manufacturing capacity for orders of various sizes.



Limited Warranty

KLT provides the following limited warranty if you purchased the Product(s) directly from KLT company or from KLT's website, <u>www.KaiLapTech.com</u>. Product(s) purchased from other sellers or sources are not covered by this Limited Warranty. KLT guarantees that the Product(s) will be free from defects in materials and workmanship under normal use for a period of one (1) year from the date you receive the product ("Warranty Period").

For all Product(s) that contain or develop material defects in materials or workmanship during the Warranty Period, KLT will, at its sole option, either: (i) repair the Product(s); (ii) replace the Product(s) with a new or refurbished Product(s) (replacement Product(s) being of identical model or functional equivalent); or (iii) provide you a refund of the price you paid for the Product(s).

This Limited Warranty of KLT is solely limited to repair and/or replacement on the terms set forth above. KLT is not reliable or responsible for any subsequential events.



KLT

CMOS CAMERA MODULES

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KLT Strength

Powerful Factory



Professional Service



Promised Delivery



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